

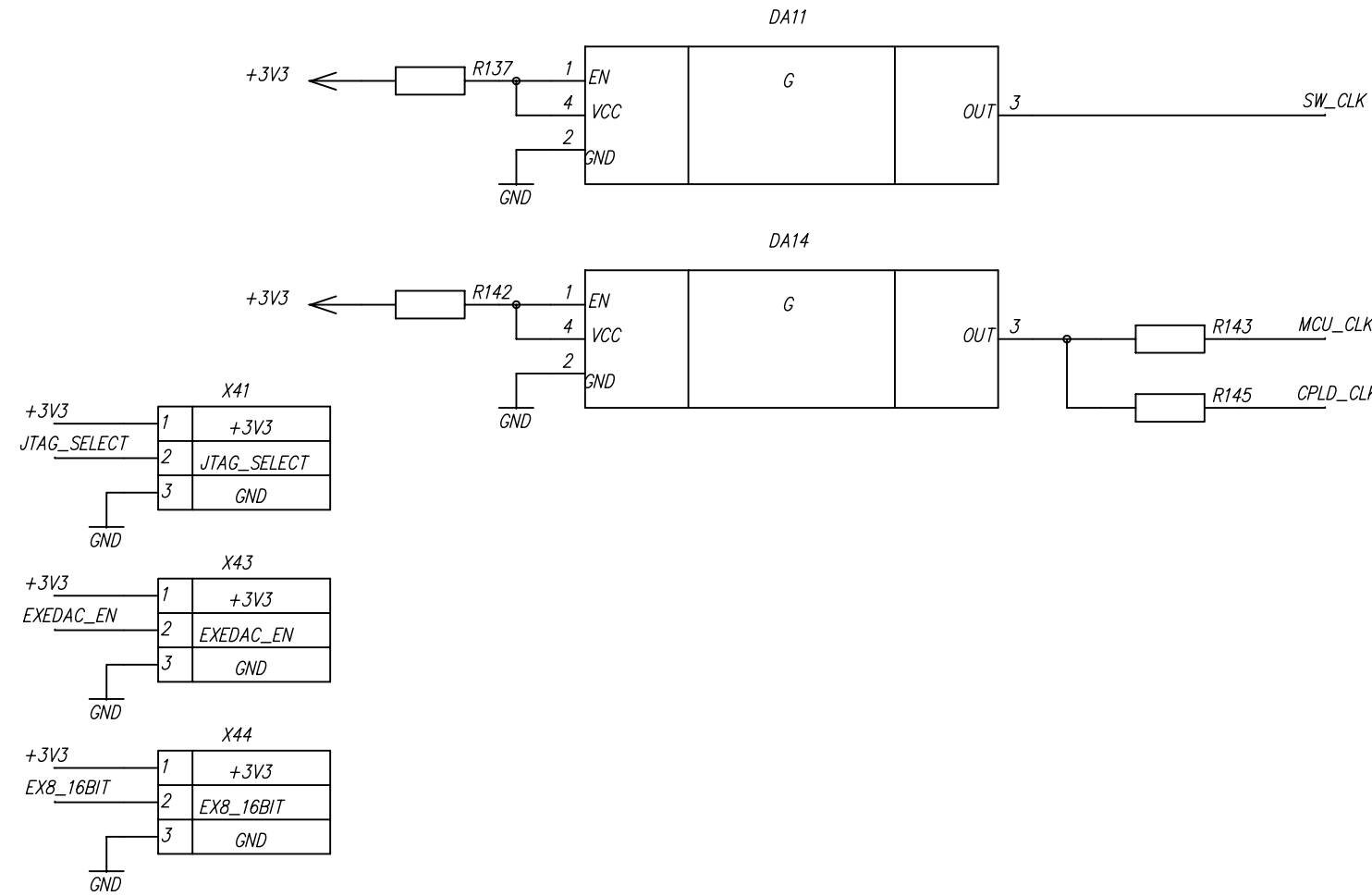
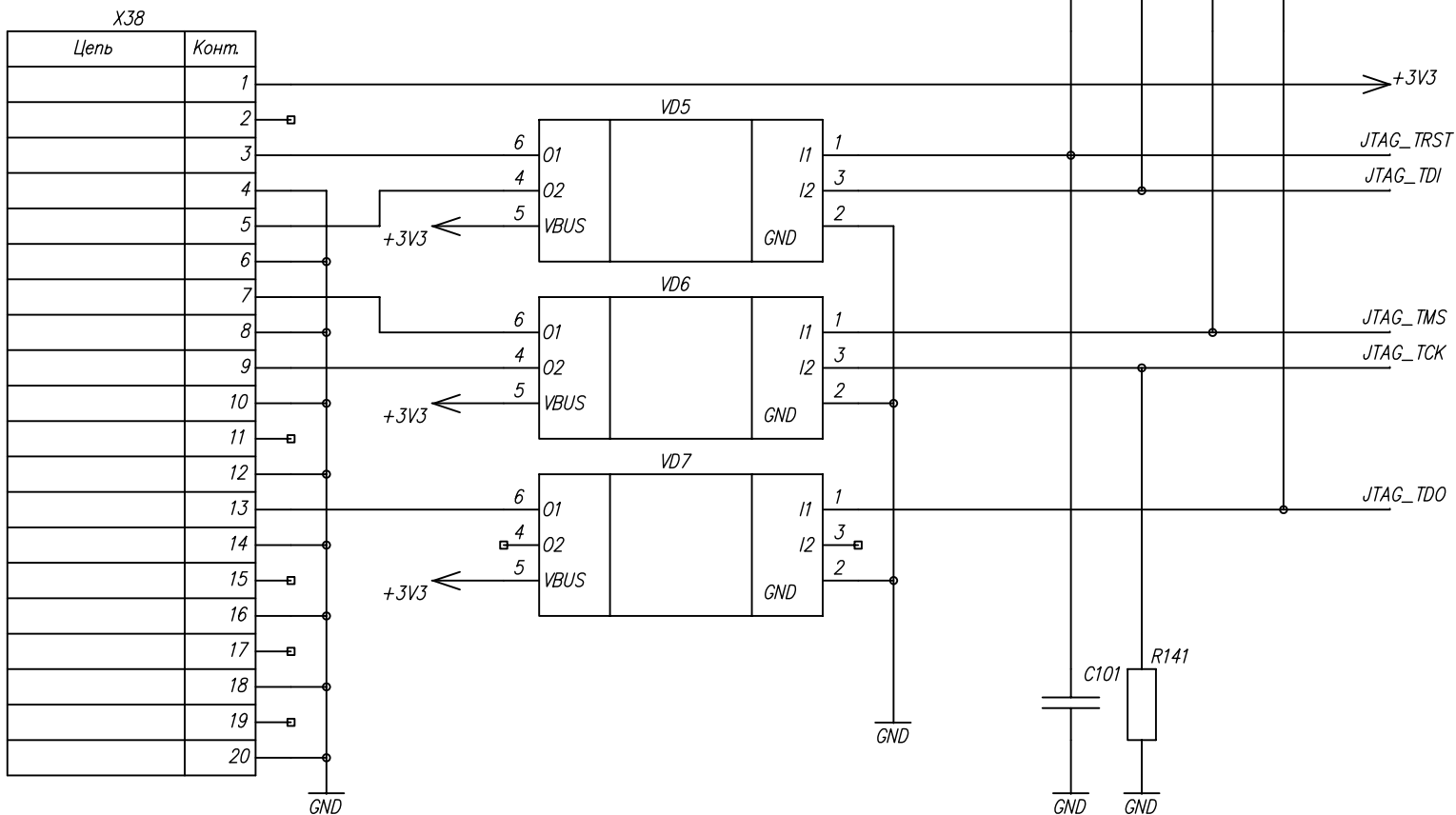
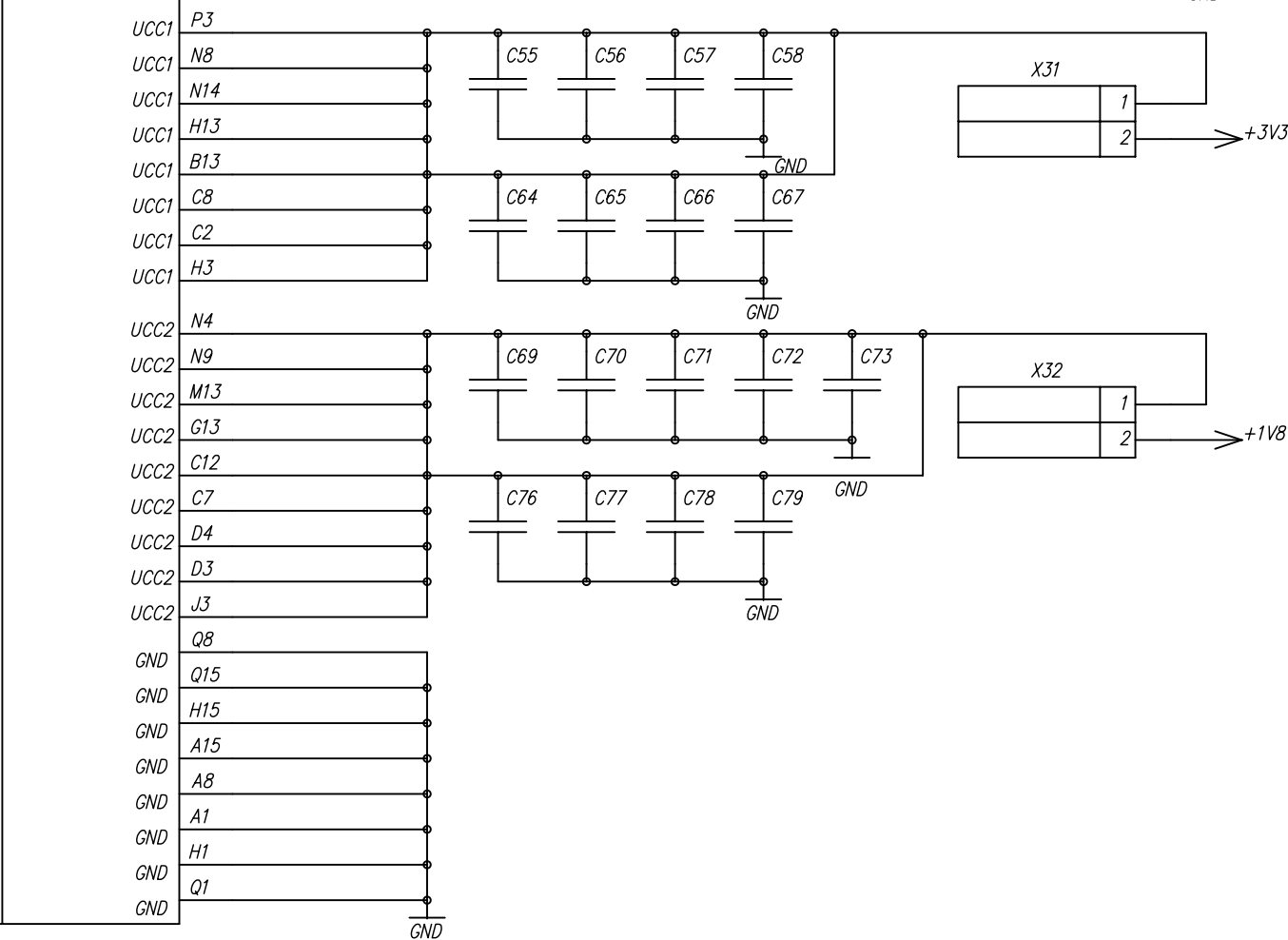
Перечень элементов ЦКРФ.467239.005 ПЗЗ

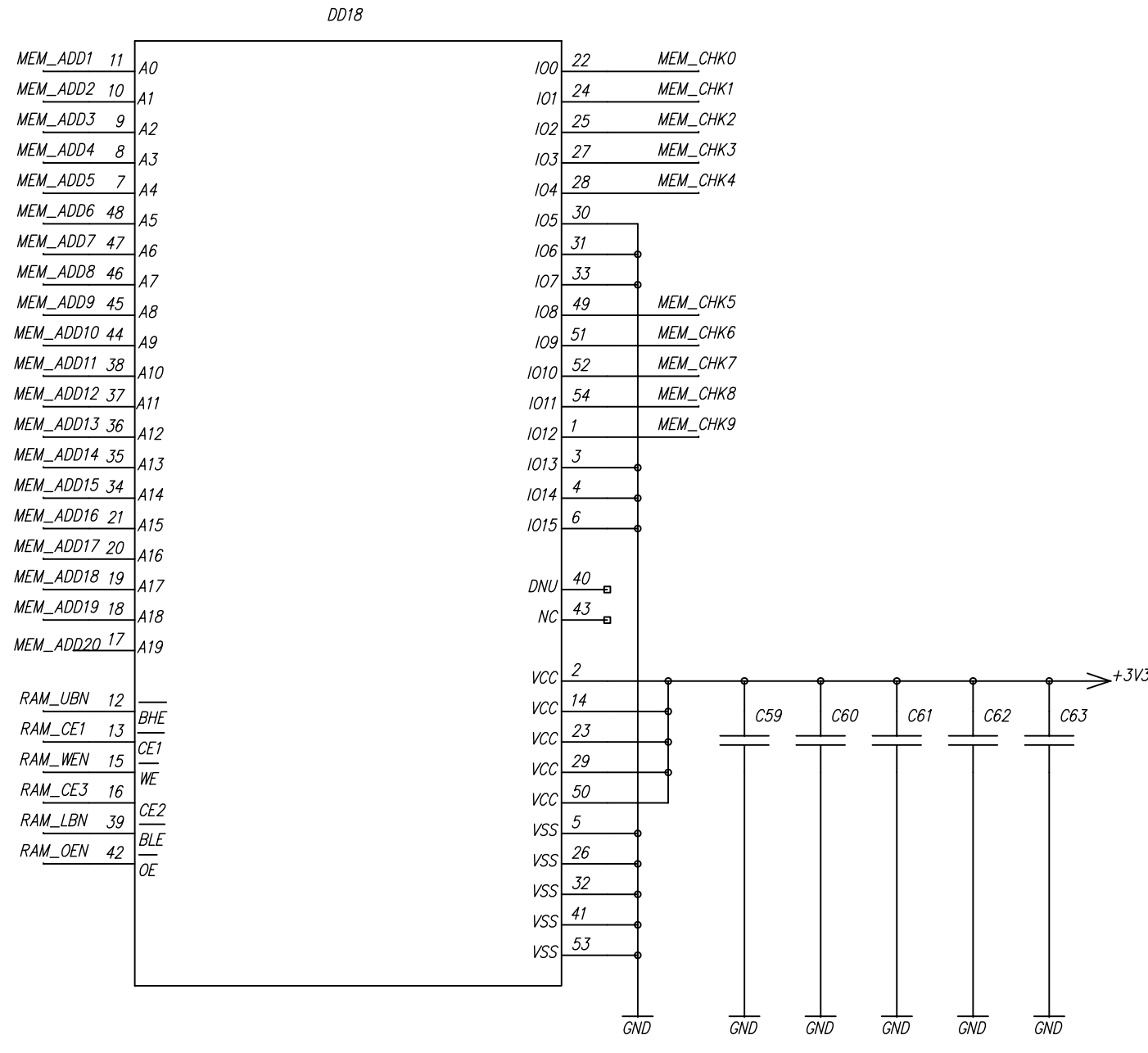
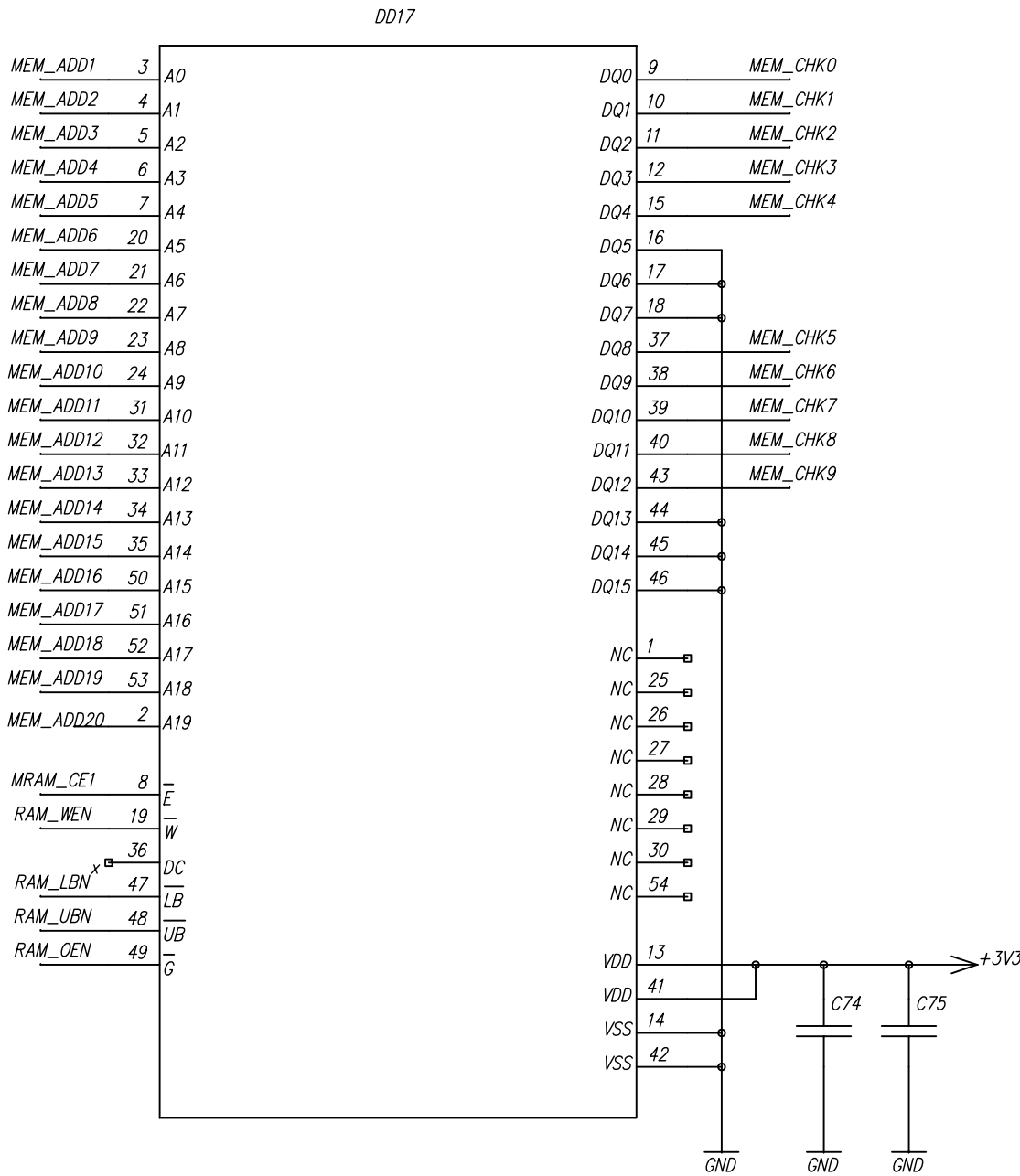
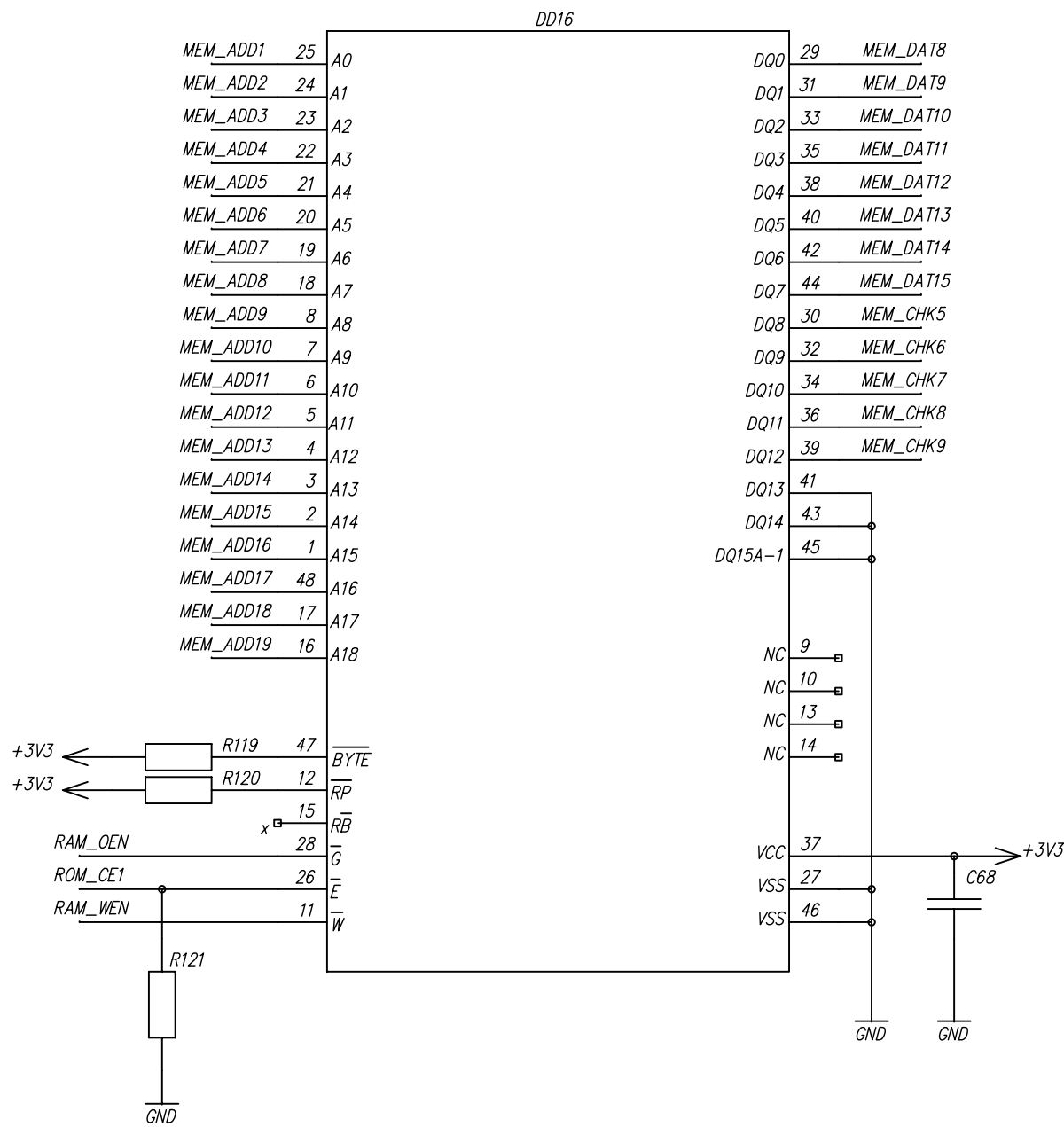
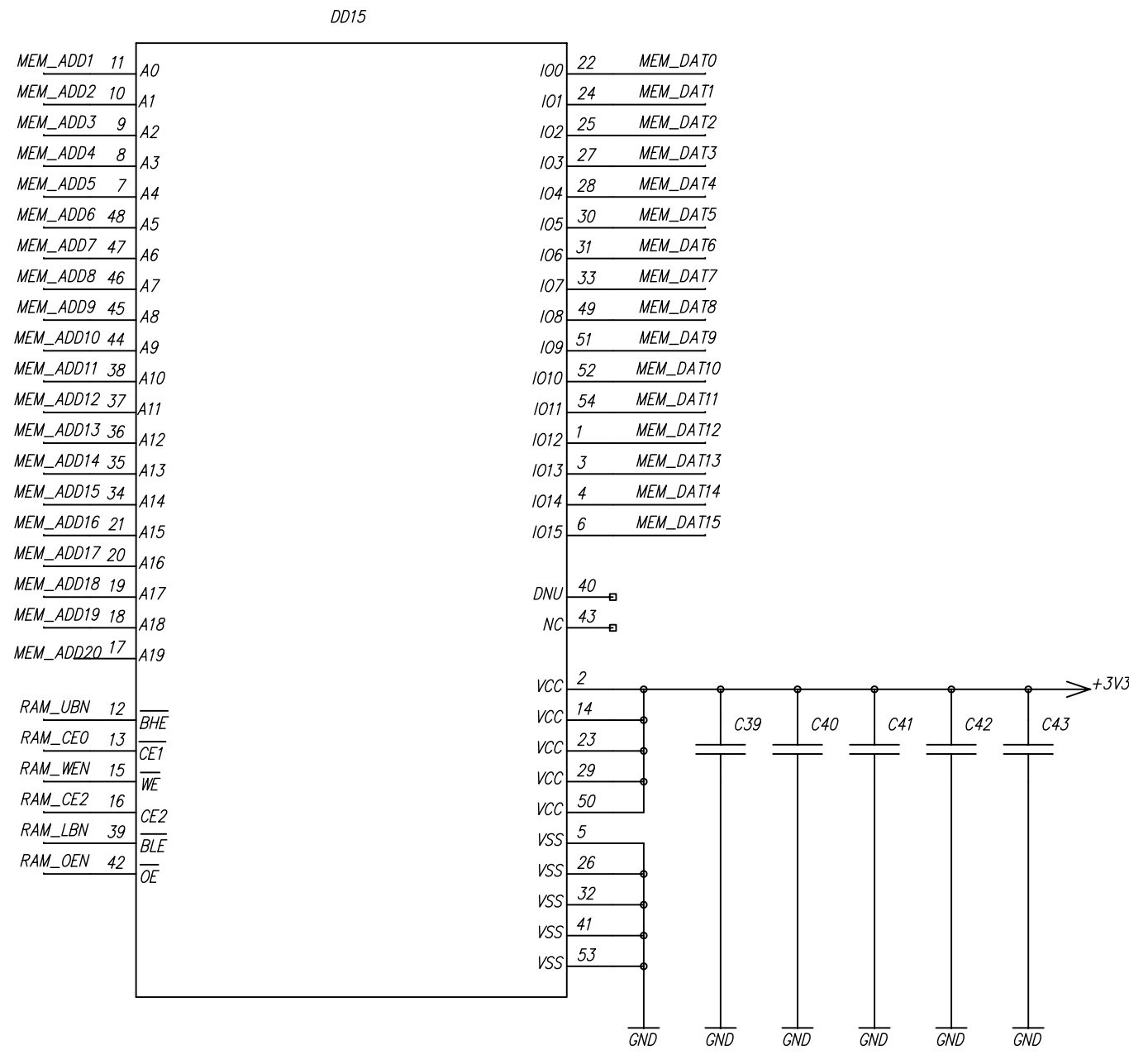
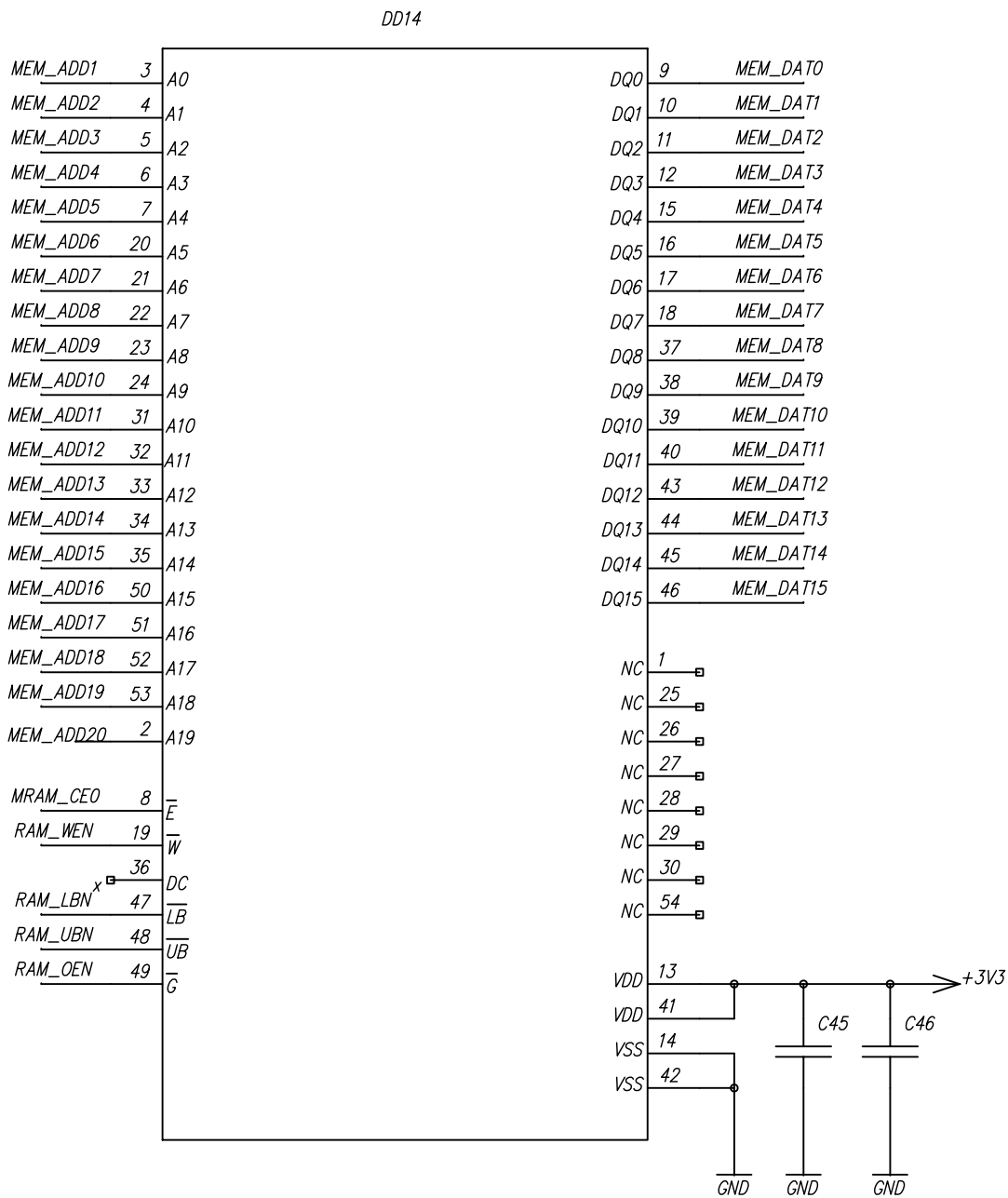
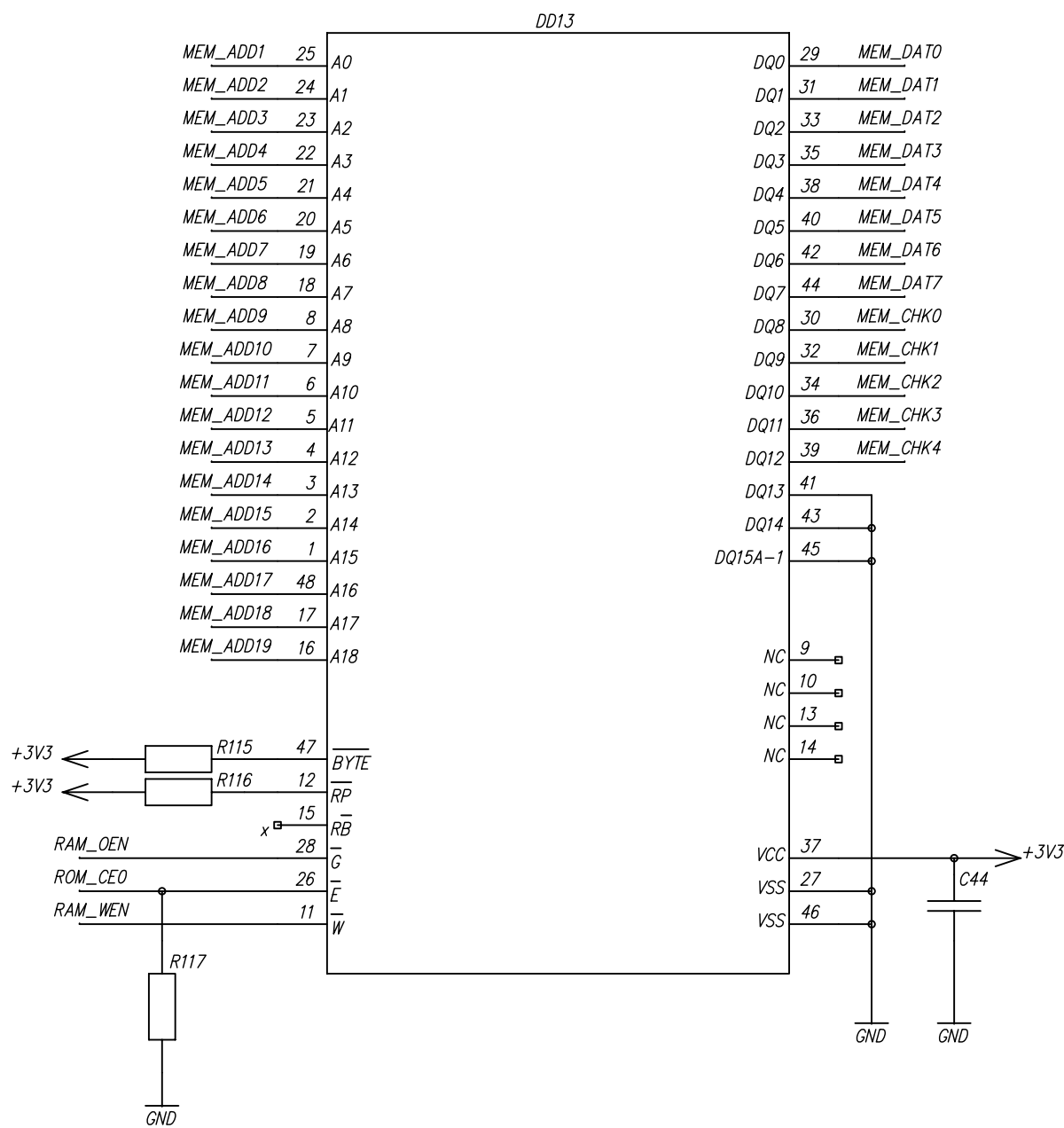
					ЦКРФ.467239.005 ЭЗ								
					Демонстрационная плата для микросхемы 5023BC016, "Спутник"				Лит.		Масса	Масштаб	
Изм	Лист	N докум.	Подп.	Дата									1:1
Разраб.													
Провер.													
Т. контр.					Схема электрическая принципиальная				Лист 1		Листов 8		
Н. контр.													
Утв.													

JTAG_TRST	N5	JTAG_TRST
JTAG_TCK	Q3	JTAG_TCK
JTAG_TMS	P5	JTAG_TMS
JTAG_TDI	Q4	JTAG_TDI
JTAG_TDO	N6	JTAG_TDO
JTAG_SELECT	N12	JTAG_SELECT
MCU_CLK	Q2	CLK
RST	P4	RST
MKPD1_RX_A	C10	MKPD1_RX_A
MKPD1_NRX_A	B10	MKPD1_nRX_A
MKPD1_RX_B	A10	MKPD1_RX_B
MKPD1_NRX_B	A9	MKPD1_nRX_B
MKPD1_TX	A11	MKPD1_TX
MKPD1_nTX	B9	MKPD1_nTX
MKPD1_TXEN_A	C9	MKPD1_TXEN_A
MKPD1_TXEN_B	A6	MKPD1_TXEN_B
MKPD2_RX_A	B7	MKPD2_RX_A
MKPD2_NRX_A	B6	MKPD2_nRX_A
MKPD2_RX_B	A4	MKPD2_RX_B
MKPD2_NRX_B	A3	MKPD2_nRX_B
MKPD2_TX	C6	MKPD2_TX
MKPD2_nTX	A5	MKPD2_nTX
MKPD2_TXEN_A	B5	MKPD2_TXEN_A
MKPD2_TXEN_B	B3	MKPD2_TXEN_B
MKPD3_RX_A	A2	MKPD3_RX_A
MKPD3_NRX_A	C4	MKPD3_nRX_A
MKPD3_RX_B	D2	MKPD3_RX_B
MKPD3_NRX_B	E3	MKPD3_nRX_B
MKPD3_TX	C3	MKPD3_TX
MKPD3_nTX	B2	MKPD3_nTX
MKPD3_TXEN_A	B1	MKPD3_TXEN_A
MKPD3_TXEN_B	D1	MKPD3_TXEN_B
MKPD4_RX_A	F3	MKPD4_RX_A
MKPD4_NRX_A	F2	MKPD4_nRX_A
MKPD4_RX_B	F1	MKPD4_RX_B
MKPD4_NRX_B	G1	MKPD4_nRX_B
MKPD4_TX	E1	MKPD4_TX
MKPD4_nTX	G2	MKPD4_nTX
MKPD4_TXEN_A	G3	MKPD4_TXEN_A
MKPD4_TXEN_B	K1	MKPD4_TXEN_B
TC_DIN0	P2	TC_DIN[0]
TC_DIN1	H2	TC_DIN[1]
TC_DIN2	J1	TC_DIN[2]
TC_ACT	N3	TC_ACT
TC_CLK1	M3	TC_CLK1
TM_CLK1	L3	TM_CLK1
TM_CLK0	N2	TM_CLK0
TM_DOUT	P1	TM_DOUT
SW_DOUT0	L1	SW_DOUT[0]
SW_SOUT0	K3	SW_SOUT[0]
SW_DIN0	K2	SW_DIN[0]
SW_SIN0	J2	SW_SIN[0]
SW_DOUT1	M2	SW_DOUT[1]
SW_SOUT1	N1	SW_SOUT[1]
SW_DIN1	M1	SW_DIN[1]
SW_SIN1	L2	SW_SIN[1]
SW_CLK	B8	SW_CLK
UART1_RXD	B4	UART1_RxD
UART1_TXD	A7	UART1_TxD
UART2_RXD	C1	UART2_RxD
UART2_TXD	C5	UART2_TxD
SPI1_MISO	A14	SPI1_MISO
SPI1_MOSI	B12	SPI1_MOSI
SPI1_SCK	B14	SPI1_SCK
SPI1_SS	C13	SPI1_SS
SPI2_MISO	B11	SPI2_MISO
SPI2_MOSI	A12	SPI2_MOSI
SPI2_SCK	A13	SPI2_SCK
SPI2_SS	C11	SPI2_SS

DD11

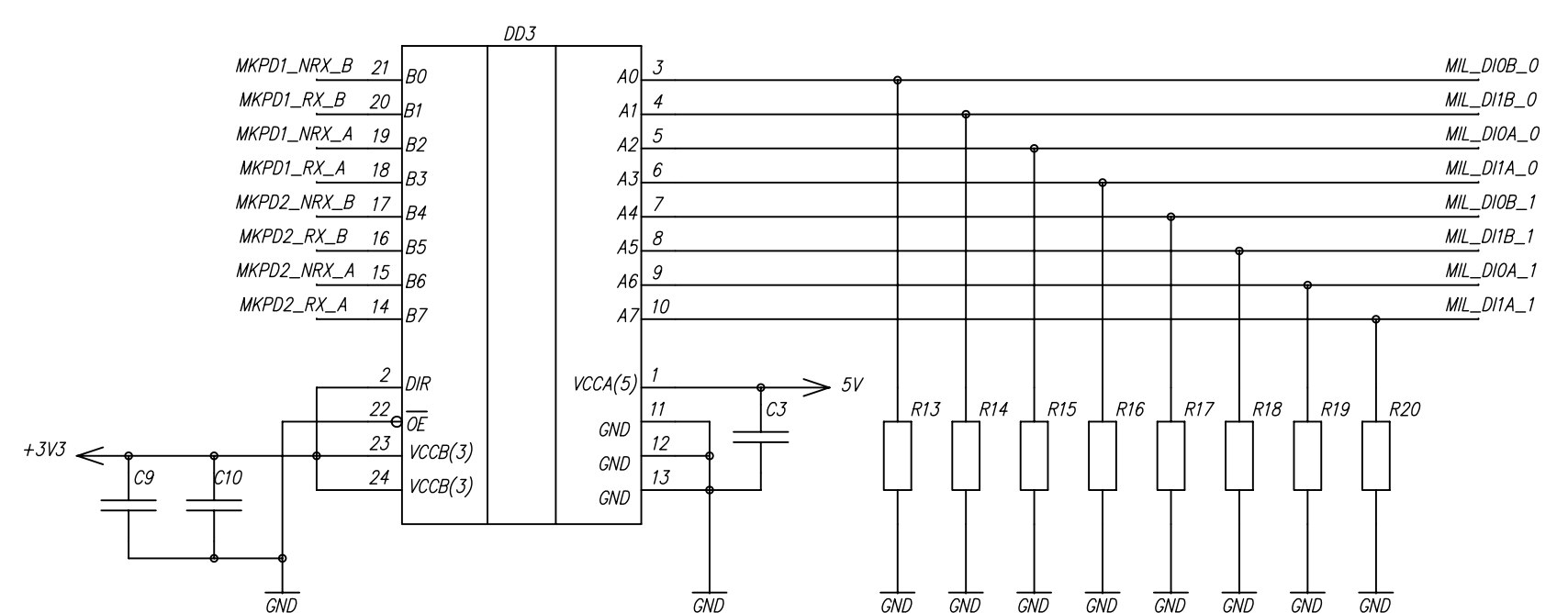
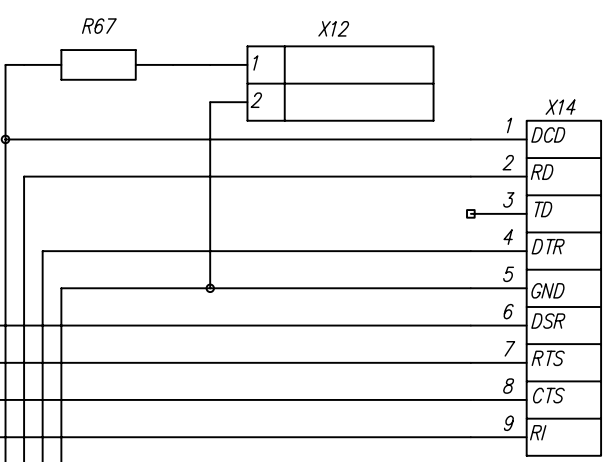
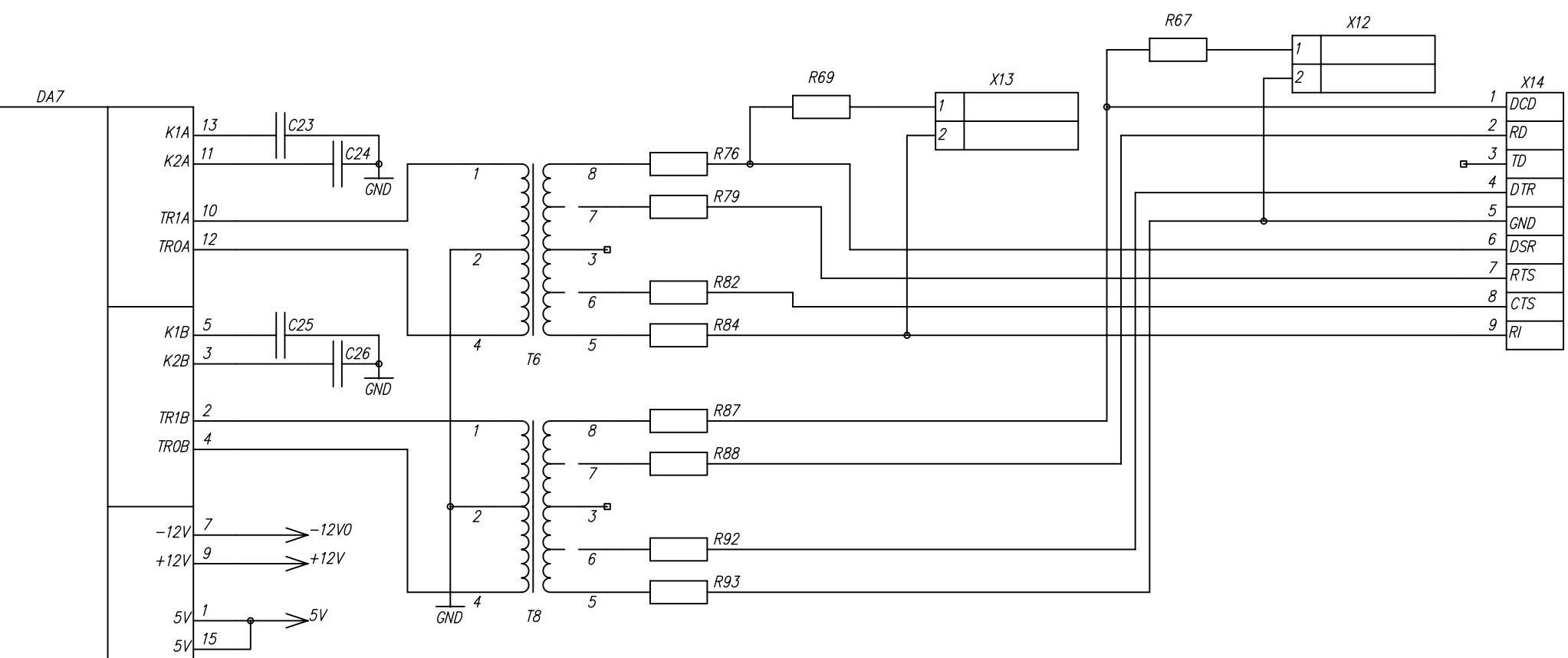
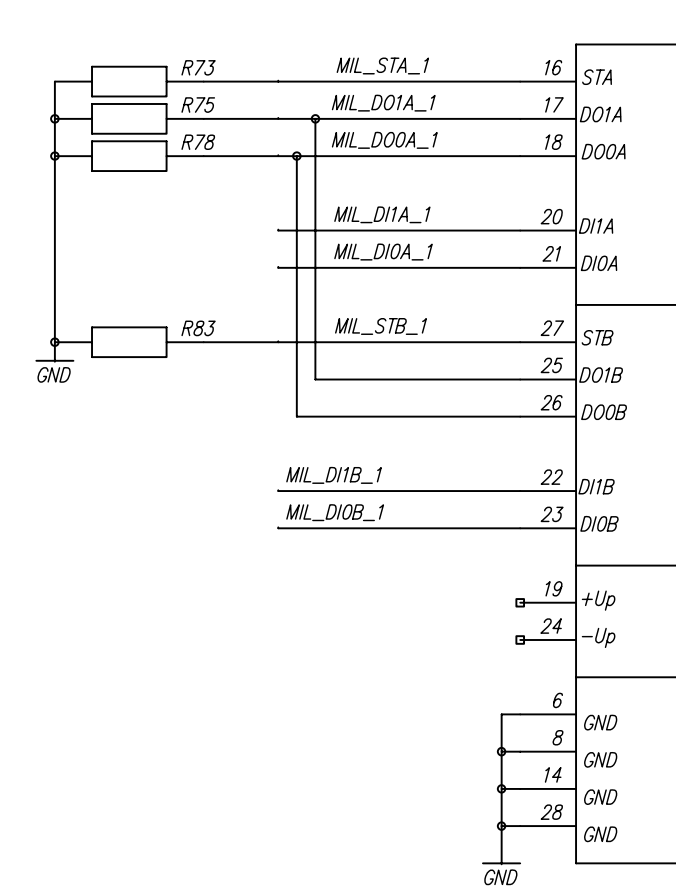
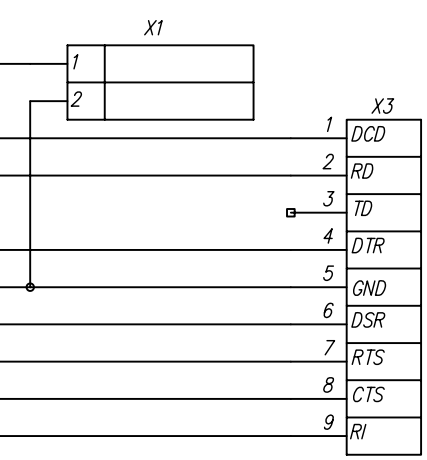
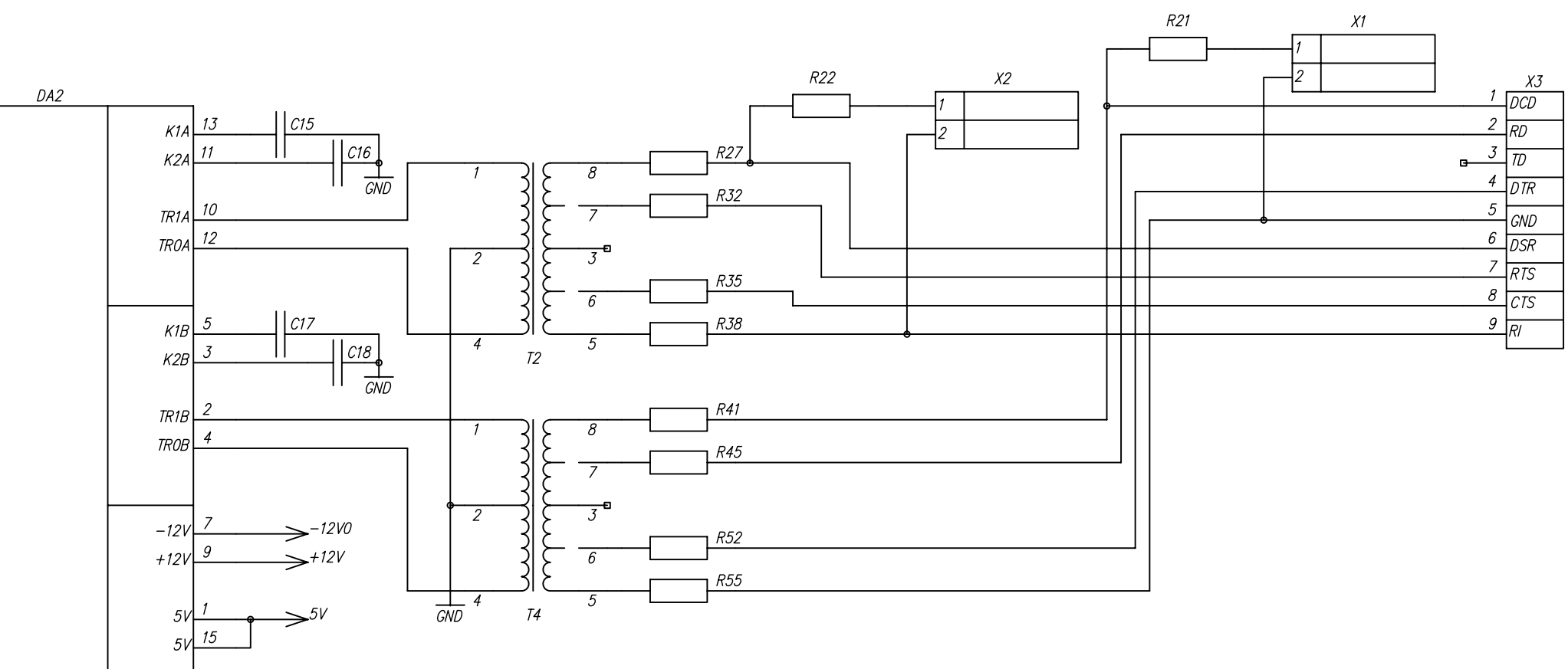
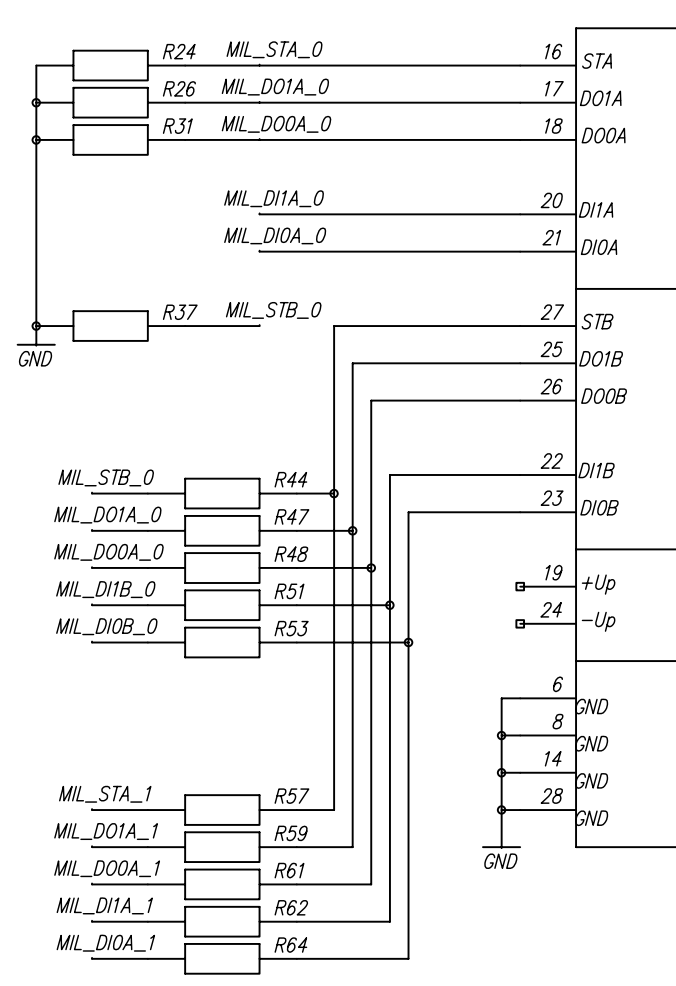
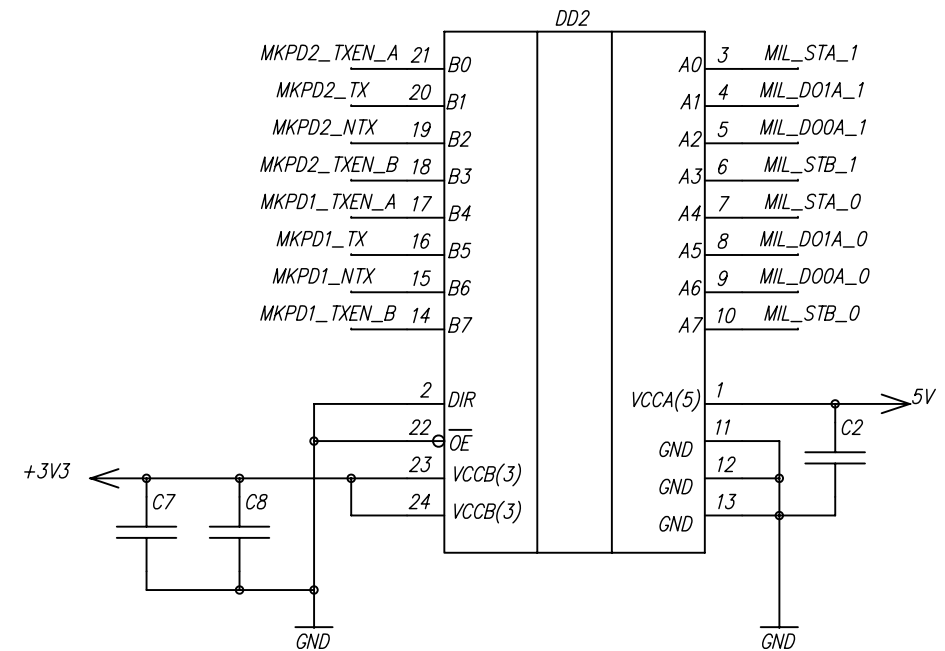
RAM_ADDR[0]	P7	MEM_ADD0
RAM_ADDR[1]	N7	MEM_ADD1
RAM_ADDR[2]	Q6	MEM_ADD2
RAM_ADDR[3]	Q7	MEM_ADD3
RAM_ADDR[4]	P8	MEM_ADD4
RAM_ADDR[5]	Q9	MEM_ADD5
RAM_ADDR[6]	Q10	MEM_ADD6
RAM_ADDR[7]	P9	MEM_ADD7
RAM_ADDR[8]	P10	MEM_ADD8
RAM_ADDR[9]	N10	MEM_ADD9
RAM_ADDR[10]	Q11	MEM_ADD10
RAM_ADDR[11]	P11	MEM_ADD11
RAM_ADDR[12]	Q12	MEM_ADD12
RAM_ADDR[13]	Q13	MEM_ADD13
RAM_ADDR[14]	P12	MEM_ADD14
RAM_ADDR[15]	N11	MEM_ADD15
RAM_ADDR[16]	P13	MEM_ADD16
RAM_ADDR[17]	Q14	MEM_CTRL
RAM_DATA[0]	N13	MEM_DAT0
RAM_DATA[1]	P14	MEM_DAT1
RAM_DATA[2]	P15	MEM_DAT2
RAM_DATA[3]	M14	MEM_DAT3
RAM_DATA[4]	L13	MEM_DAT4
RAM_DATA[5]	N15	MEM_DAT5
RAM_DATA[6]	L14	MEM_DAT6
RAM_DATA[7]	M15	MEM_DAT7
RAM_DATA[8]	K13	MEM_DAT8
RAM_DATA[9]	K14	MEM_DAT9
RAM_DATA[10]	L15	MEM_DAT10
RAM_DATA[11]	J14	MEM_DAT11
RAM_DATA[12]	J13	MEM_DAT12
RAM_DATA[13]	K15	MEM_DAT13
RAM_DATA[14]	J15	MEM_DAT14
RAM_DATA[15]	H14	MEM_DAT15
RAM_DATA_CHK[0]	G15	MEM_CHK0
RAM_DATA_CHK[1]	F15	MEM_CHK1
RAM_DATA_CHK[2]	G14	MEM_CHK2
RAM_DATA_CHK[3]	F14	MEM_CHK3
RAM_DATA_CHK[4]	F13	MEM_CHK4
RAM_DATA_CHK[5]	E15	MEM_CHK5
RAM_DATA_CHK[6]	E14	MEM_CHK6
RAM_DATA_CHK[7]	D15	MEM_CHK7
RAM_DATA_CHK[8]	C15	MEM_CHK8
RAM_DATA_CHK[9]	D14	MEM_CHK9
RAM_Weh	E13	RAM_WEN
RAM_Oeh	C14	RAM_OEN
RAM_LBh	B15	RAM_LBN
RAM_UBh	D13	RAM_UBN
EXEDAC_En	P6	EXEDAC_EN
Ex8_16bit	Q5	EX8_16BIT
INTMEM_EDAC_FERR	E2	INTMEM_EDAC_FERR





Инд. N подл. Погр. и гата

Изм.	Лист	N докум.	Погр.	Дата



Изм.	Лист	N докум.	Подп.	Дата

